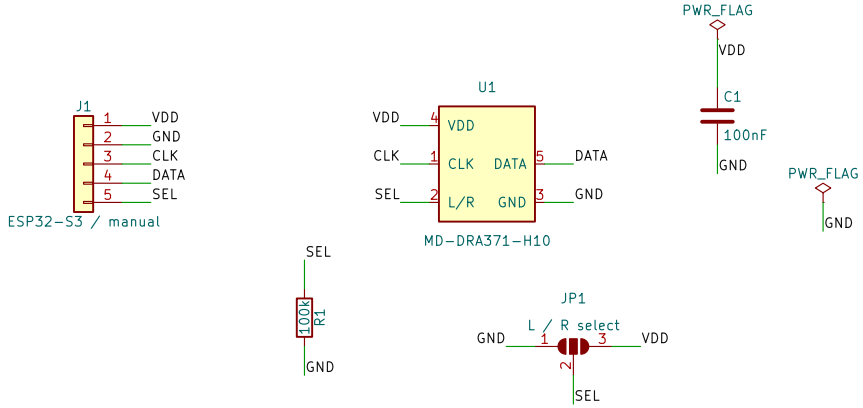
MD-DRA371-H10 PDM microphone breakout

3.3V ESP32-S3 interface | rev 2 | 2026-10-16

J1: 1 VDD / 2 GND / 3 CLK / 4 DATA / 5 SEL



SEL open = GND via 100k (DATA1). Drive SEL to VDD for DATA2.
No pull resistor on DATA. Clock damping belongs at ESP32-S3.
U1: 0.8mm NPTH acoustic port; keep rear opening unobstructed.
C1: short front-copper paths to both mic supply pins; no vias.

SMT: U1 C1 R1 on front. J1 manually fitted.
VDD = 1.6-3.3V; GPIO voltage must match VDD.

JP1 (normally open): 1-2 = L (GND); 2-3 = R (VDD).
Leave both sides open for GPIO control / default L.
Bridge one side only; disconnect external SEL drive when strapped.

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