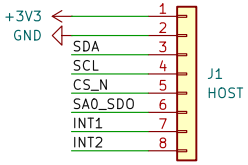
LSM6DSV16BX hand-assembly breakout

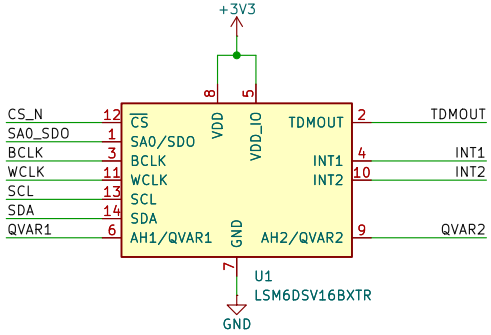
3.3V SUPPLY / I/O ONLY | all SMT on top



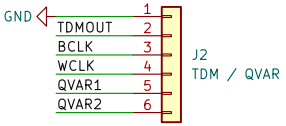
HOST



SENSOR

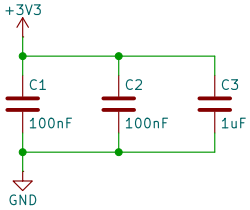


TDM / ANALOG

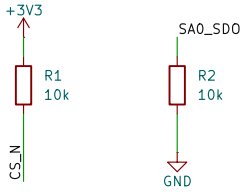


WCLK uses its internal pull-up in Mode 1.
TDM mode needs host BCLK/WCLK and receiver.

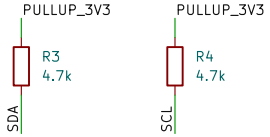
DECOUPLING



Defaults: I2C, 0x6A

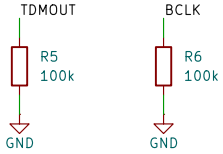




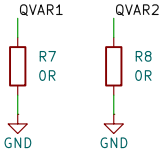


JP1 closed: local 4.7k I2C pull-ups.
Cut JP1 for SPI / I3C / external pull-ups.

TDM idle bias



Qvar / analog straps



R7/R8 fitted: Qvar pins grounded.
REMOVE before Qvar or analog testing.

Bring-up: current-limited 3.3V, scan 0x6A; read WHO_AM_I (0x0F) = 0x71.
CS high enables I2C. SA0 high selects 0x6B. No regulator or level shifting.

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