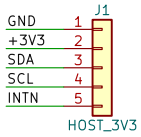
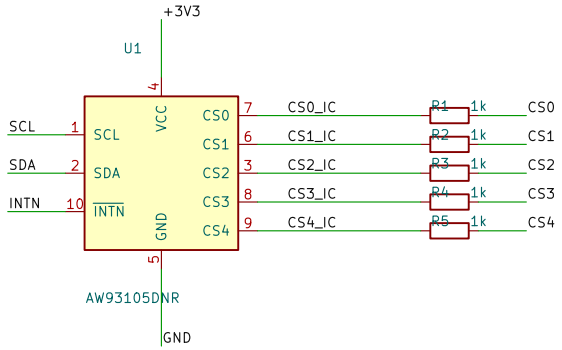
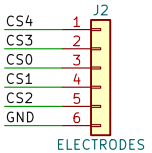
AW93x05 electrode test breakout — 3.3 V

U1: AW93105DNR fitted; AW93205DNR / AW93305DNR share this DNR pinout and land pattern.
Different firmware/configuration is required for each IC. No BFOR variants.



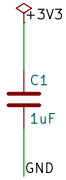
J1: regulated 3.3 V and 3.3 V logic only.
7-bit I2C default: 0x12. INTN is open-drain, active low.

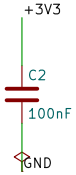




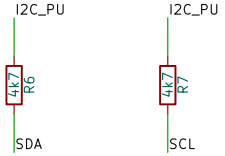
R1–R5: 1k default. Replace with 0R only for a channel configured as driven shield.
CS2 must remain unstrapped for normal five-channel/default-address operation.











JP1 closed by copper as manufactured. Cut bridge to disable both I2C pull-ups.
R8 (INTN) stays fitted. Use the same supply for VCC and all pull-ups.

Ear test: T1 -> CS0, T2 -> CS1, T3 -> CS2; CS3/CS4 available for reference/shield.
Short independent wires, no connection through the ear's J1 LED FFC.
Bench prototype: no external TVS fitted; handle electrodes with ESD precautions.

Sheet: / File: aw93x05.kicad_sch		
Title:		
Size: A4	Date:	**Rev:**
KiCad E.D.A. 10.0.4		Id: 1/1